

L Number	Hits	Search Text	DB	Time stamp
-	982	(independent\$2 or simultaneous\$2 or concurrent\$2) near3 initializ\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/04 10:35
-	1	(lens near2 (controller or micro-controller or microcontroller or micro-computer or microcomputer)) same (camera near2 (controller or micro-controller or microcontroller or micro-computer or microcomputer)) same ((independent\$2 or simultaneous\$2 or concurrent\$2) near3 initializ\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/04 10:41
-	12	US-5115064-\$.DID. OR US-6229954-\$.DID. OR US-5721987-\$.DID. OR US-4521678-\$.DID. OR US-5309195-\$.DID. OR US-6356307-\$.DID.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/04 10:39
-	565	(lens near2 (controller or micro-controller or microcontroller or micro-computer or microcomputer)) same (camera near2 (controller or micro-controller or microcontroller or micro-computer or microcomputer))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/04 10:41
-	2	((lens near2 (controller or micro-controller or microcontroller or micro-computer or microcomputer)) same (camera near2 (controller or micro-controller or microcontroller or micro-computer or microcomputer))) and ((independent\$2 or simultaneous\$2 or concurrent\$2) near3 initializ\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/04 10:41
-	9	multitask\$3 with routine with independent\$3	USPAT; EPO; JPO; DERWENT	2004/10/20 09:24
-	2	multithread\$3 with routine with independent\$3	USPAT; EPO; JPO; DERWENT	2004/10/20 09:27
-	1	multi-thread\$3 with routine with independent\$3	USPAT; EPO; JPO; DERWENT	2004/10/20 09:28
-	2	multi-thread\$3 same routine with independent\$3	USPAT; EPO; JPO; DERWENT	2004/10/20 11:23
-	1	((concurrent\$3 or simultaneous\$2) near4 initializ\$3) same (multitask\$3 or multi-task\$3 or multithread\$3 or multi-thread\$3)	USPAT; EPO; JPO; DERWENT	2004/10/20 10:04
-	640	independent\$3 near4 initializ\$6	USPAT; EPO; JPO; DERWENT	2004/10/20 11:25

-	2	(independent\$3 near4 initializ\$6) with (multitask\$3 or multi-task\$3 or multithread\$3 or multi-thread\$3)	USPAT; EPO; JPO; DERWENT	2004/10/20 09:43
-	3	(independent\$3 near4 initializ\$6) same (multitask\$3 or multi-task\$3 or multithread\$3 or multi-thread\$3)	USPAT; EPO; JPO; DERWENT	2004/10/20 09:40
-	20	(independent\$3 near4 initializ\$6) with (CPU or microcomputer or microprocessor or microcontroller)	USPAT; EPO; JPO; DERWENT	2004/10/20 09:54
-	7	(independent\$3 near4 initializ\$6) same camera	USPAT; EPO; JPO; DERWENT	2004/10/20 09:59
-	35	(independent\$3 near4 initializ\$6) and camera	USPAT; EPO; JPO; DERWENT	2004/10/20 10:00
-	11	(initializ\$6 with (multitask\$3 or multi-task\$3 or multithread\$3 or multi-thread\$3)) and camera	USPAT; EPO; JPO; DERWENT	2004/10/20 10:06
-	2	(initializ\$6 with (multitask\$3 or multi-task\$3 or multithread\$3 or multi-thread\$3)) with independent\$2	USPAT; EPO; JPO; DERWENT	2004/10/20 10:07
-	104	initializ\$6 with (multitask\$3 or multi-task\$3 or multithread\$3 or multi-thread\$3)	USPAT; EPO; JPO; DERWENT	2004/10/20 10:33
-	43	independent\$3 with routine with initializ\$6	USPAT; EPO; JPO; DERWENT	2004/10/20 10:23
-	10	initializ\$6 near2 file with independent\$3	USPAT; EPO; JPO; DERWENT	2004/10/20 10:33
-	2	multithread\$3 same routine with independent\$3	USPAT; EPO; JPO; DERWENT	2004/10/20 11:23
-	5	(independent\$3 near4 initializ\$6) with (simultaneous\$2 or concurrent\$2)	USPAT; EPO; JPO; DERWENT	2004/10/20 11:26